

Notice of References Cited

Application/Control No.

10/021,173

Applicant(s)/Patent Under
Reexamination
YAMAURA ET AL.

Examiner

Leonardo Andújar

Art Unit

2826

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,989,993	11-1999	Zakel et al.	438/614
	B	US-6,195,260	02-2001	Moriyama, Yoshifumi	361/749
	C	US-6,329,065	12-2001	Ishida et al.	428/472
	D	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing For the VLSI Era, Lattice Press, Vol. 1, pages 851-859.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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